

## ABSTRACT

An improved method for determining location of an electrical short by inferring labels from schematic connectivity. In particular, a short locator tool, creates a copy of the artwork of the circuit where the short is located and may automatically infer additional labels from a schematic connectivity text file. By inferring additional labels for signal names on the copy of the artwork, the short locator tool is able to obtain the shortest path between two conflicting labels. The resulting error shape is thus much smaller and easier to diagnose. This method is particularly effective on power supplies and clock nets which are the most difficult shorts to diagnose.

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